

## SM2752

### UFS 2.1 Controller with LDPC

The SM2752 is the SMI UFS controller solution compliant with the latest UFS standard of JEDEC. With high-speed serial-link based MIPI MPHY HS-Gear-3, 2-lane and SCSI architecture model (SAM), the SM2752 enables high-performance storage accessing, better power efficiency, and ease of system design.

The SM2752 incorporates the proprietary firmware and controller technology that elevates the capabilities to integrate new algorithms that boost random sustained read and write performances. The adoption of data error detection in SRAM prevents the occurrence of soft-error event and enhances data reliability.

With superior performance, multitasking support, and high stability, the SM2752 UFS solution can ideally serve the needs of a wide variety of mobile devices and new booming embedded/portable applications.

#### Key Features

- **JEDEC UFS Standard Version 2.1 Compliant**
  - Supports HS-Gear-3 x 2-lane
  - Supports MPHY version 3.0
  - Supports UniPro standard version 1.6
- **High Performance Platform with Proprietary Flash Controller**
  - Dynamic power management technology
- **Dual Channel NAND Flash Controller**
  - 1.8V operation
  - Supports Toggle 2.0 /ONFI 3.X NAND up to 266MHz DDR, 533MB/s
- **LDPC ECC Engine**
  - Supports low-power decoding mode and high-correction capability decoding with soft information

