

SM2750

UFS 2.1 Controller

SM2750 is SMI's UFS controller solution compliant with JEDEC latest UFS standard. With high-speed serial link based MIPI M-PHY HS-GEAR3 single lane and SCSI architecture model (SAM), the SM2750 enables high-performance storage accessing, better power efficiency and ease of system design.

The SM2750 incorporates proprietary firmware and controller technology, thereby elevating the capabilities to integrate new algorithms that boost random sustained read/write performance. The adoption of data error detection in SRAM prevents occurrence of soft-error event and enhances data reliability.

With superior performance, multitasking support, and high stability, the SM2750 UFS solution can ideally serve the needs of a wide variety of portable devices and new booming embedded/mobile applications.

Key Feature

- **JEDEC UFS Standard Version 2.1 Compliant**
 - Supports HS-Gear-3 x 1-lane
 - Supports MPHY version 3.0
 - Supports UniPro standard version 1.6
- **High Performance Platform with Proprietary Flash Controller**
 - Dynamic power management technology
- **Single Channel NAND Flash Controller**
 - 1.8V operation
 - Supports Toggle 2.0 /ONFI 3.X NAND up to 266MHz DDR, 533MB/s
- **Configurable BCH ECC Engine**

